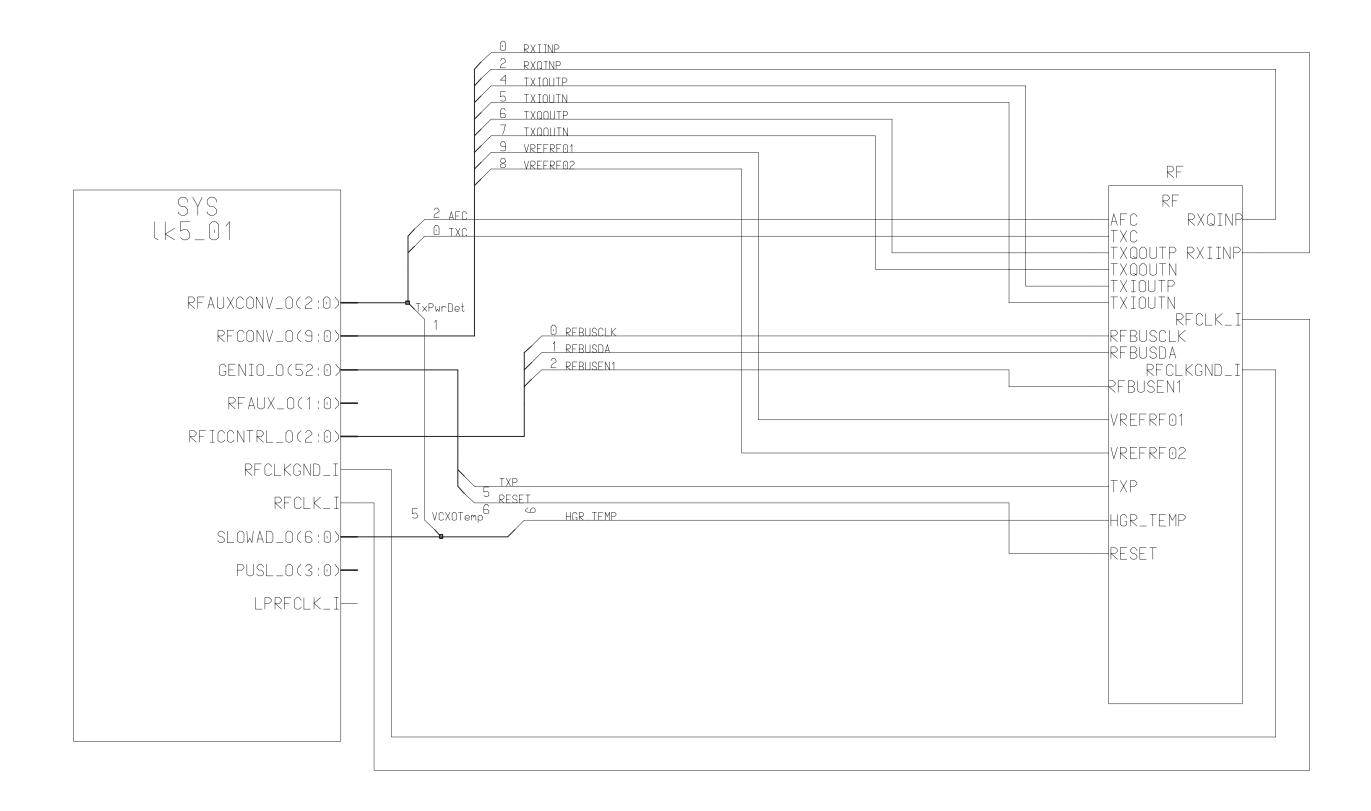
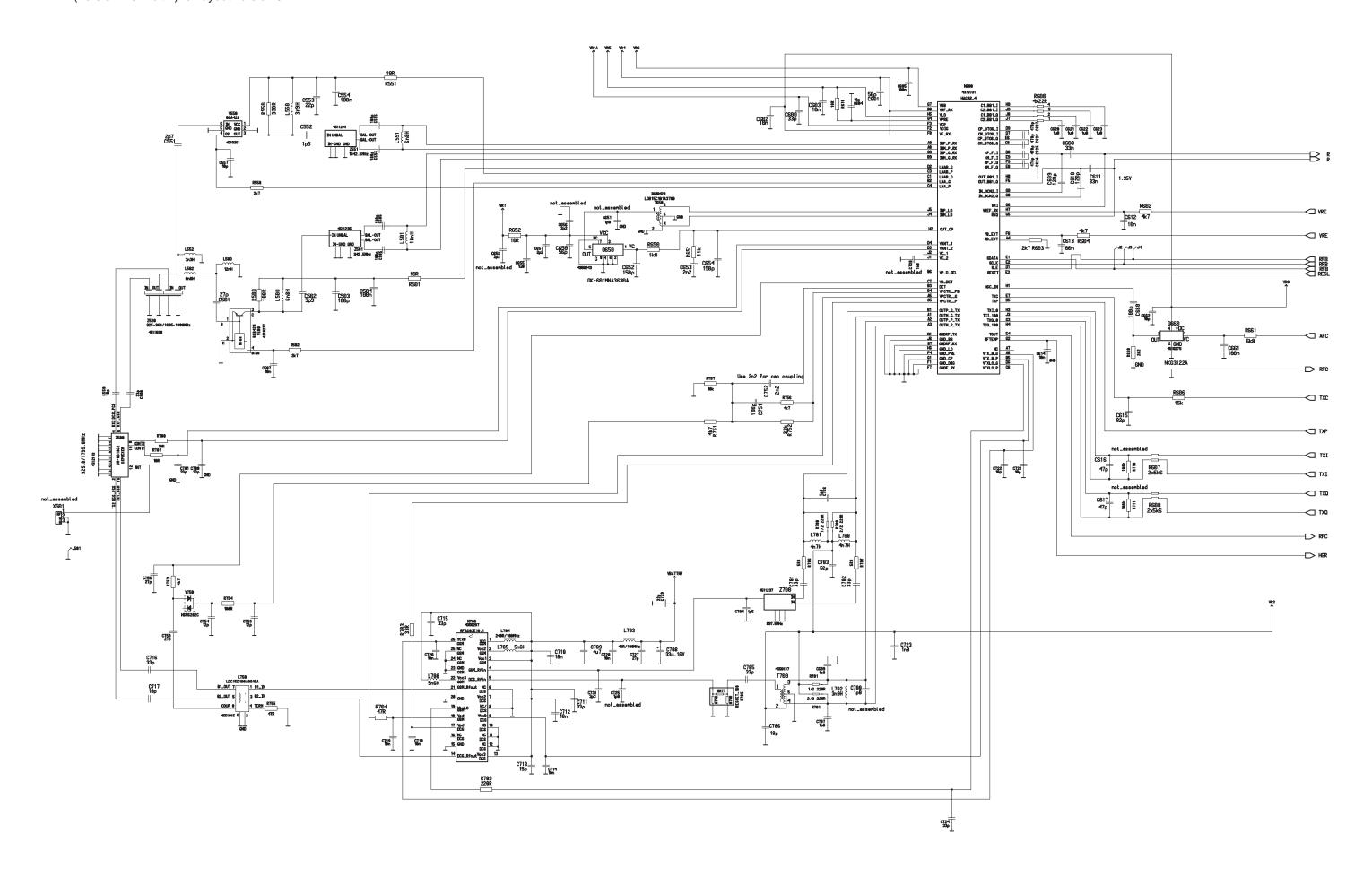
RF & BB (Version 0.0 Edit 2) for layout version 01

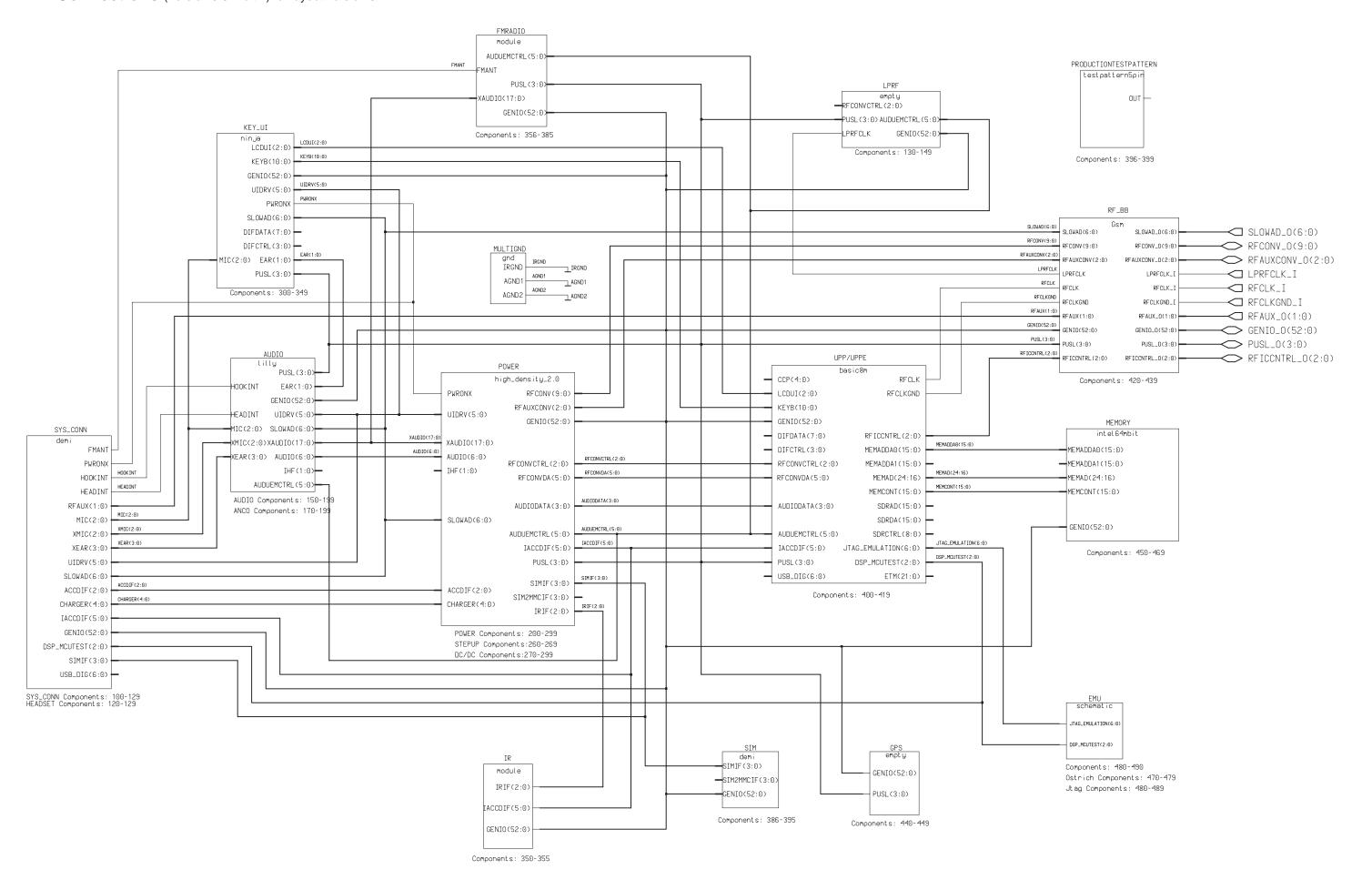


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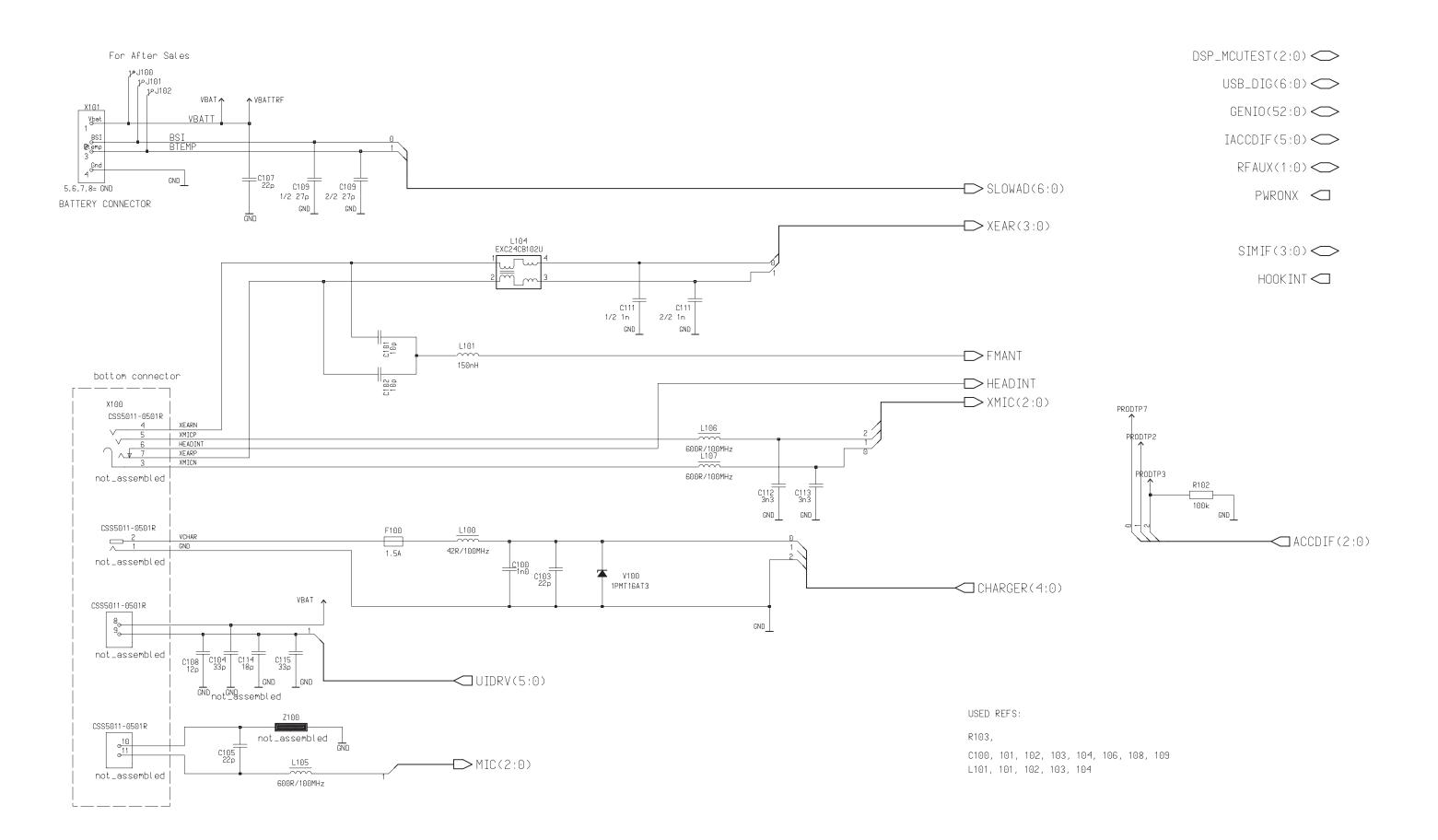
RF (Version 2.0 Edit 2) for layout version 01



BB Connections (Version 0.0 Edit 4) for layout version 01



System Connector (Version 1.3 Edit 162) for layout version 01



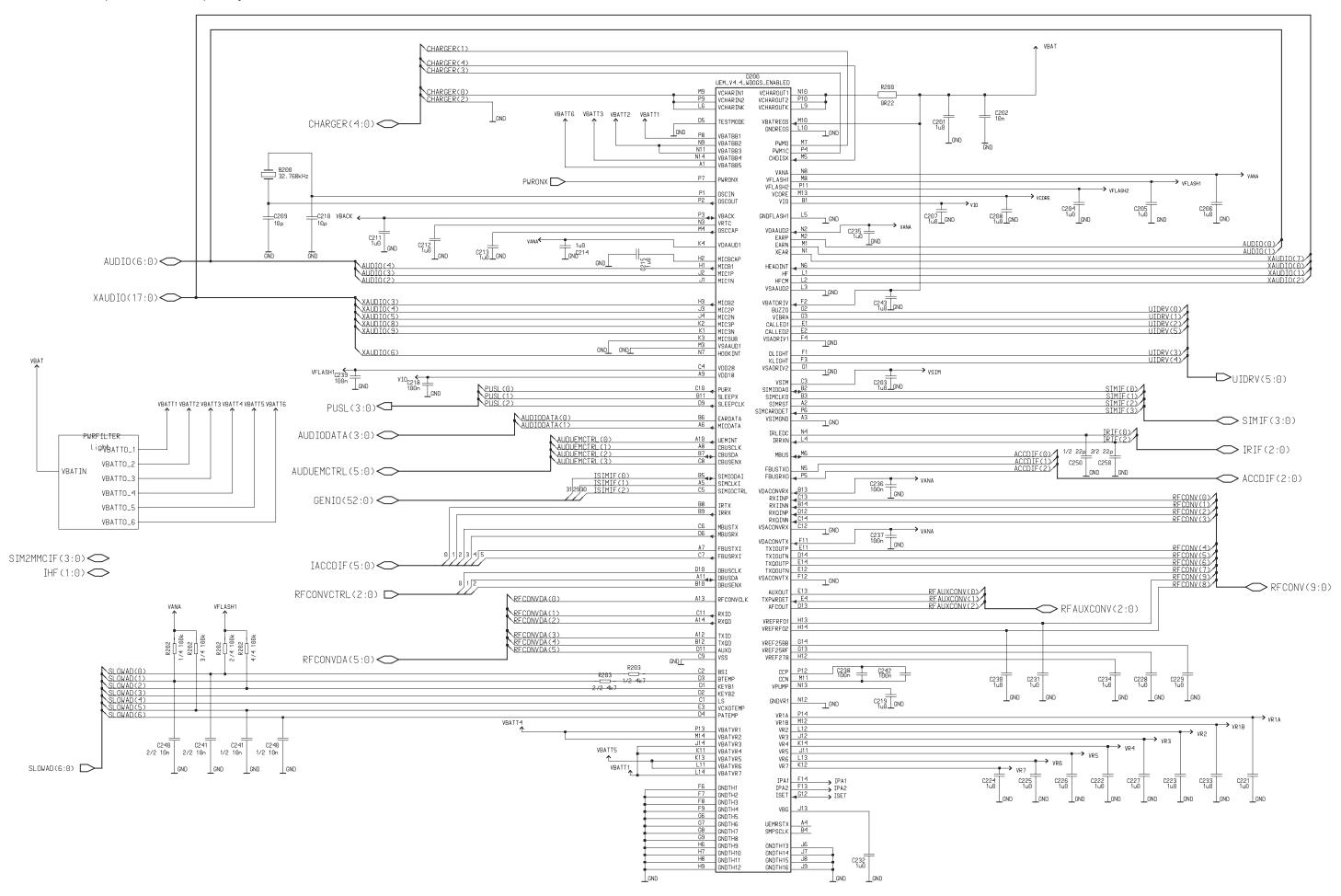
Audio Interface (Version 1.3 Edit 1) for layout version 01 \bigcirc SLOWAD(6:0) GENIO(52:0) PUSL(3:0) \bigcirc UIDRV(5:0) → HOOKINT IHF(1:0) AUDUEMCTRL(5:0) \rightarrow EAR(1:0) L151 EXC24CB102U R164 "EARP" 0 "EARP" 1/2 22R _{R164} "Close to UEM" "EARN" 2/2 22R "MICN" "MIC1N" <u>C151</u> 2x33n| "MIC1P" 1/2 33n "MICP" 0 2x2k2 2k2 R152 \bigcirc MIC(2:0) "MICB1" C156 18p = C152 27ρ C160 1/2 10n C160 2/2 10n AUDIO(6:0) C168 27p C166 100n GND GND \rightarrow XEAR(3:0) GND GND R165 "XEARN" o "HFCM" 1/2 22R "XEARP" "HF" 2/2 22R "XMICN" "MIC2N" C155 | 2x33n | 1/2 33n "XMICP" R161,
1 /2 VWM16V

2 /2 VWM16V
R161 C155 2x33n 2/2 33n C153 C153 1/2 1n 2/2 1n 0 C161 1/2 22p ✓ XMIC(2:0) C161 C163 C163 C163 C1/2 22p 2/2 22p __ GND /2 22p "MICB2" C165 L 100n __GND GND ✓ HEADINT R154 C157 1u0 10k C158 GND XAUDIO(17:0)

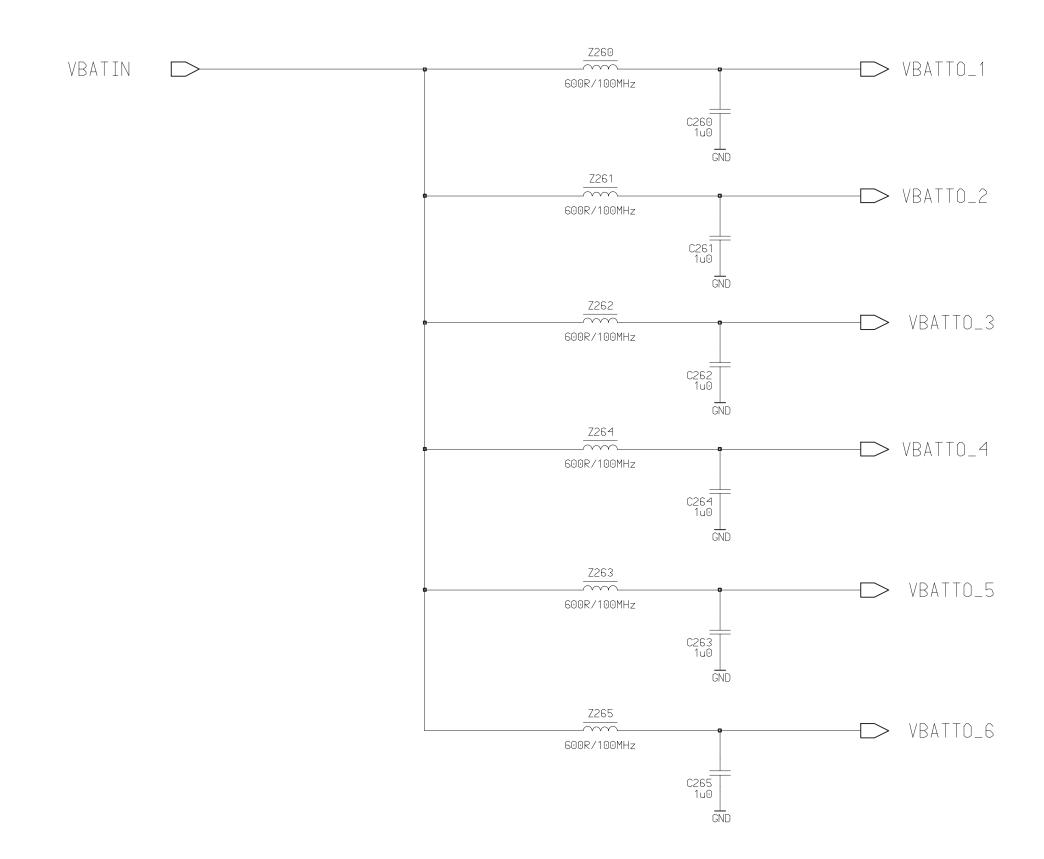
> Used Refs: C151-C153, C155- 166 R151, 153-R160, 162, 163 Z151-Z152

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UEM of BB (Version 2.0 Edit 1) for layout version 01

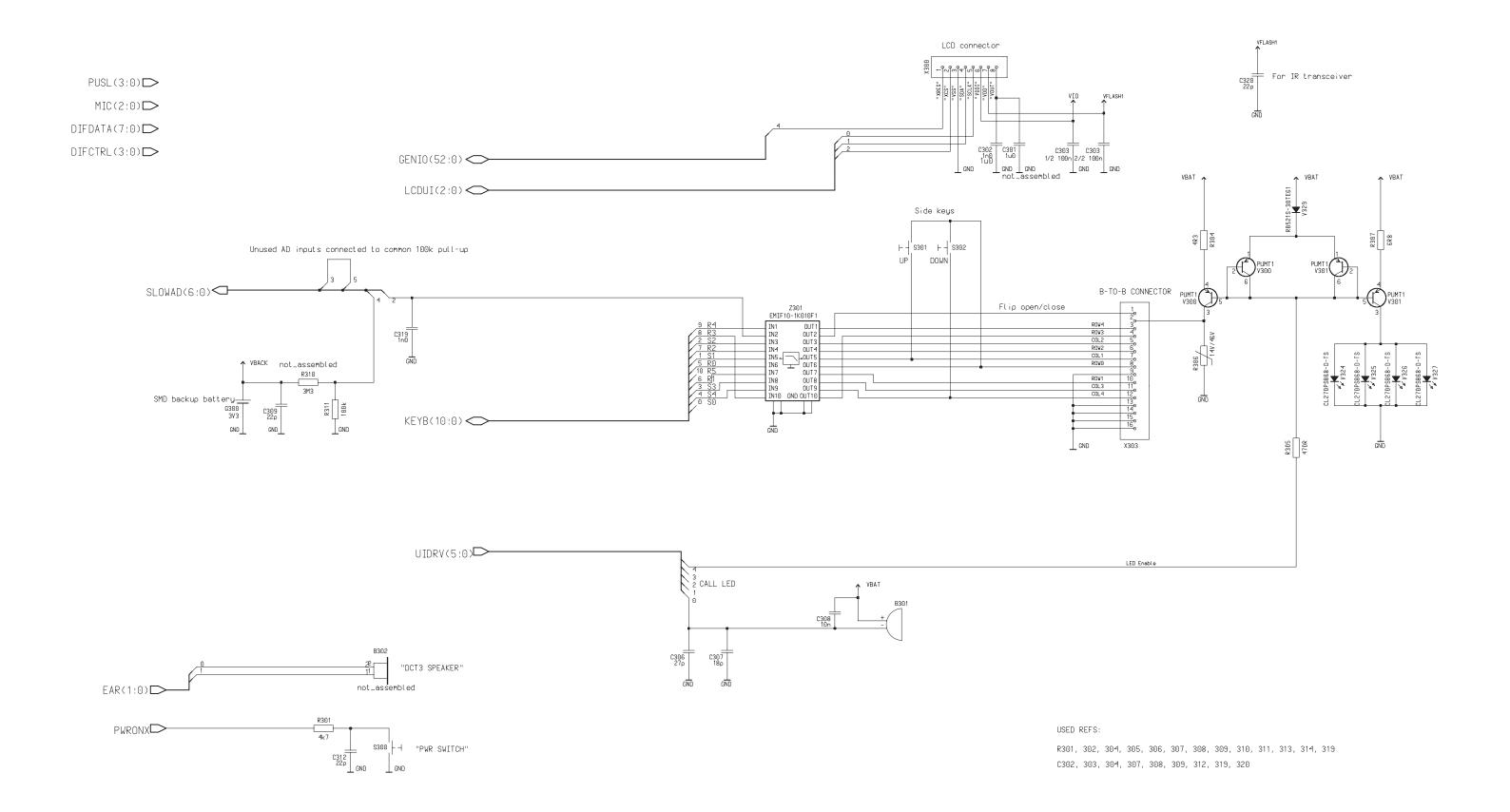


Light Filtering (Version 2.0 Edit) for layout version 01

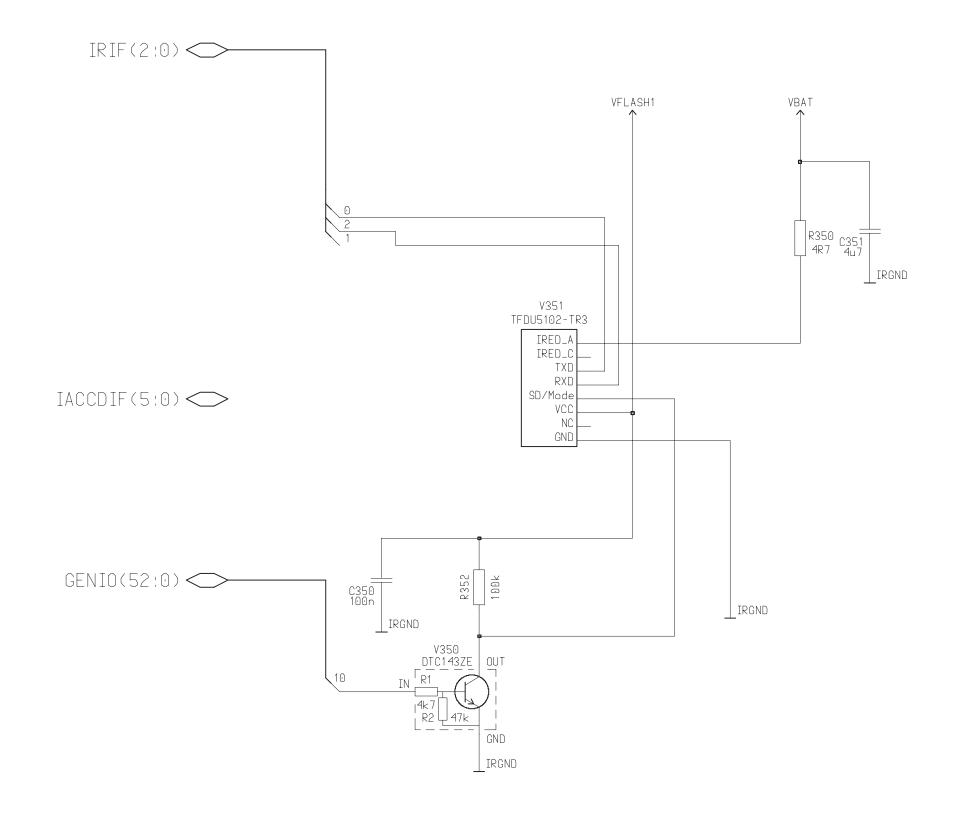


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Display and Keyboard Interface (Version 1.3 Edit 212) for layout version 01

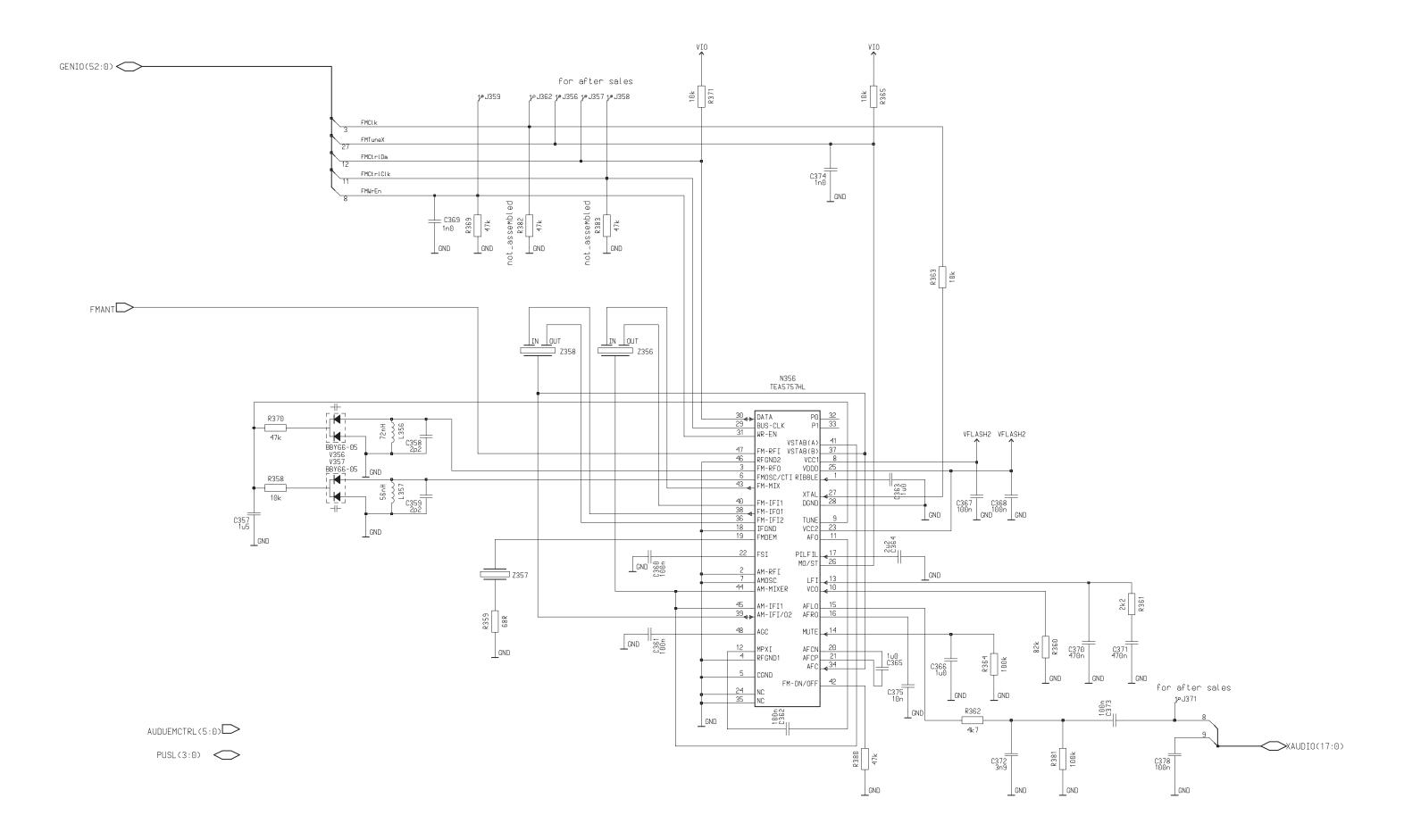


Infrared Module (Version 2.0 Edit 38) for layout version 01

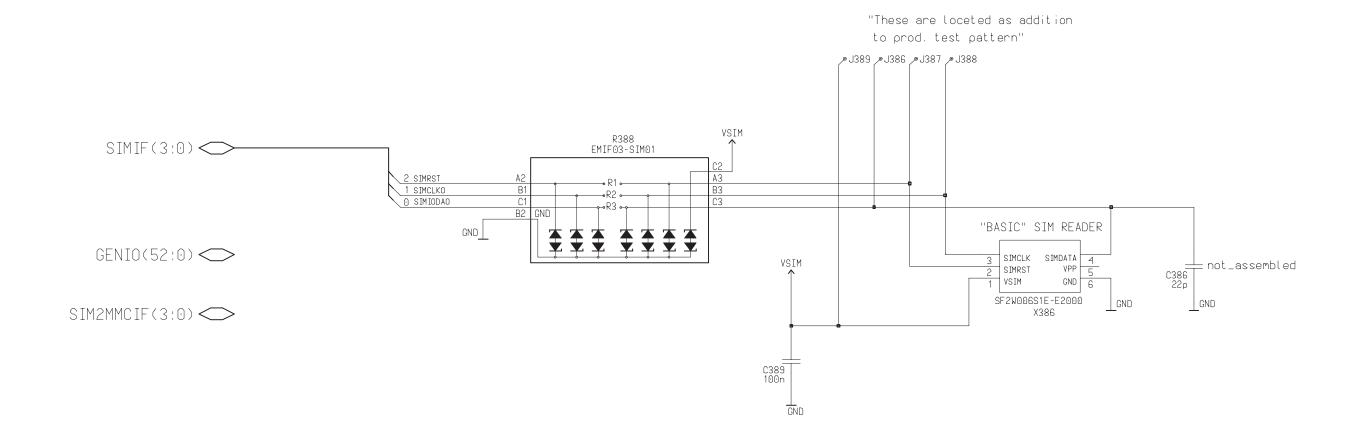


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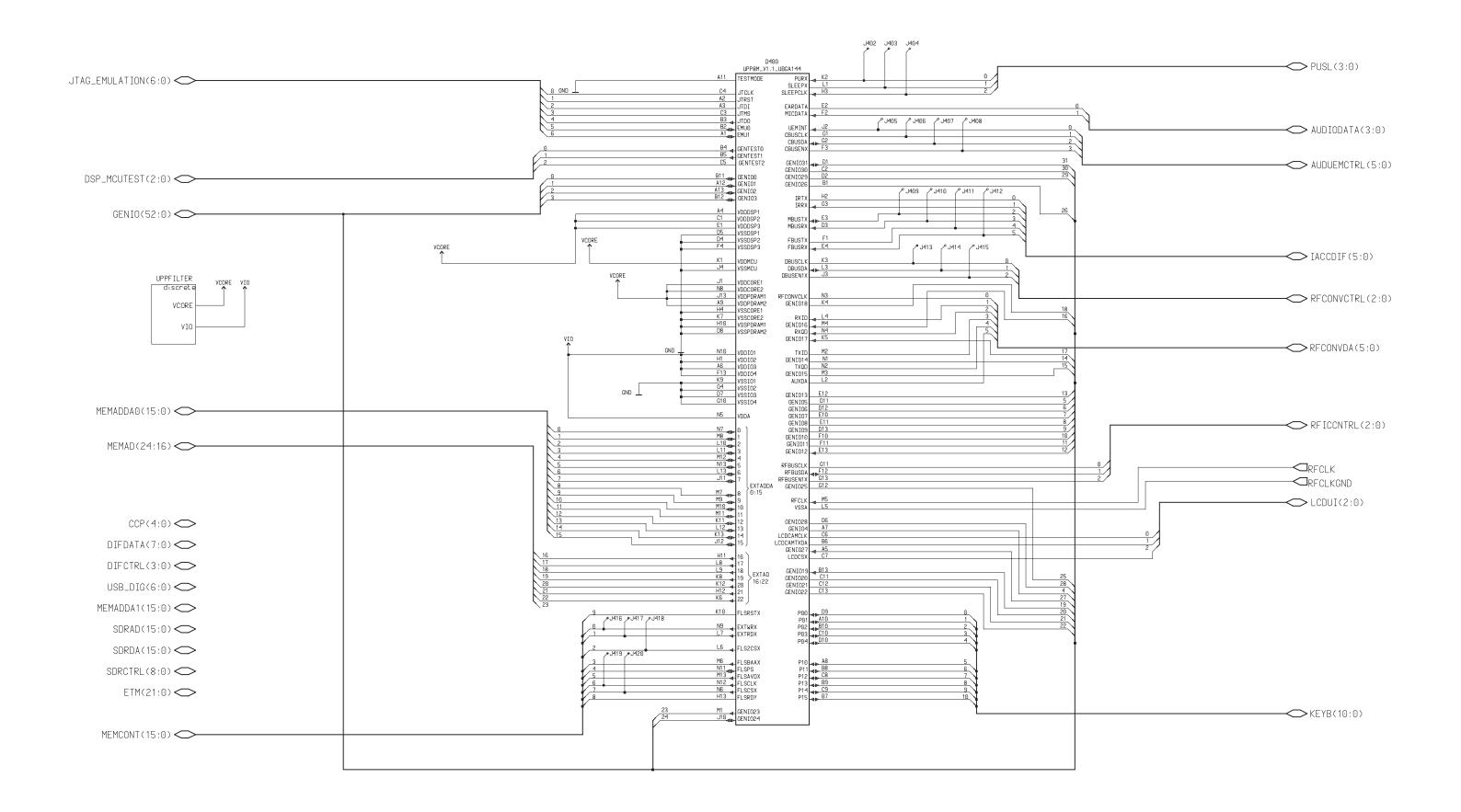
FM Radio (Version 1.3 Edit 110) for layout version 01



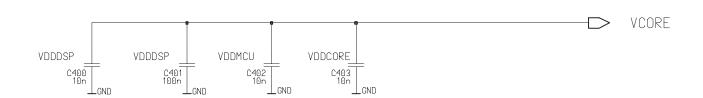
SIM Reader (Version 1.3 Edit 48) for layout version 01

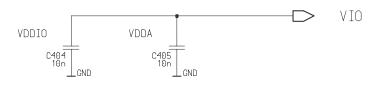


UPP and decoupling capacitors (Version 2.0 Edit 91) for layout version 01

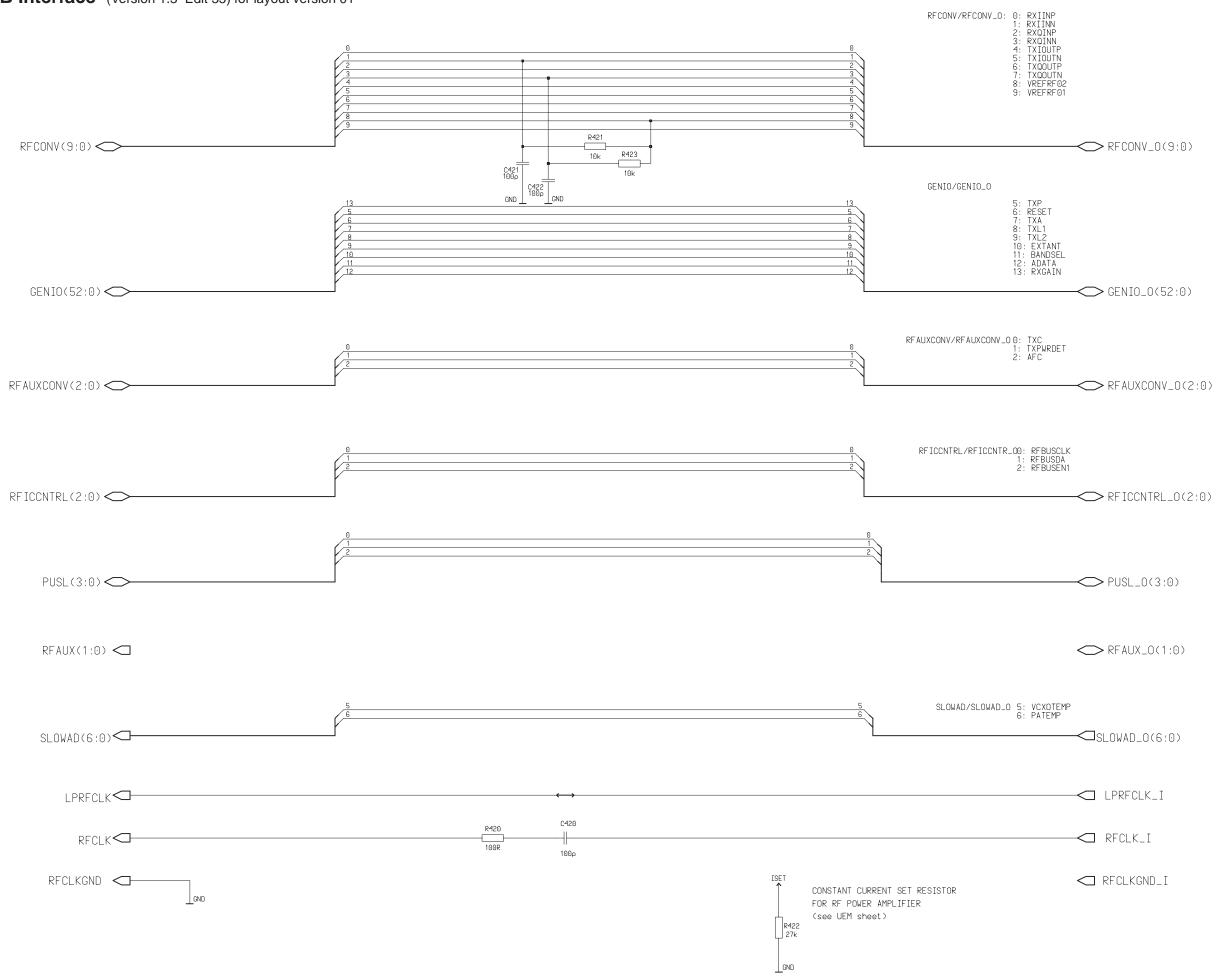


Discrete capacitors for UPP (Version 1.3 Edit 10) for layout version 01

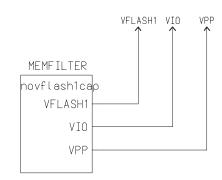


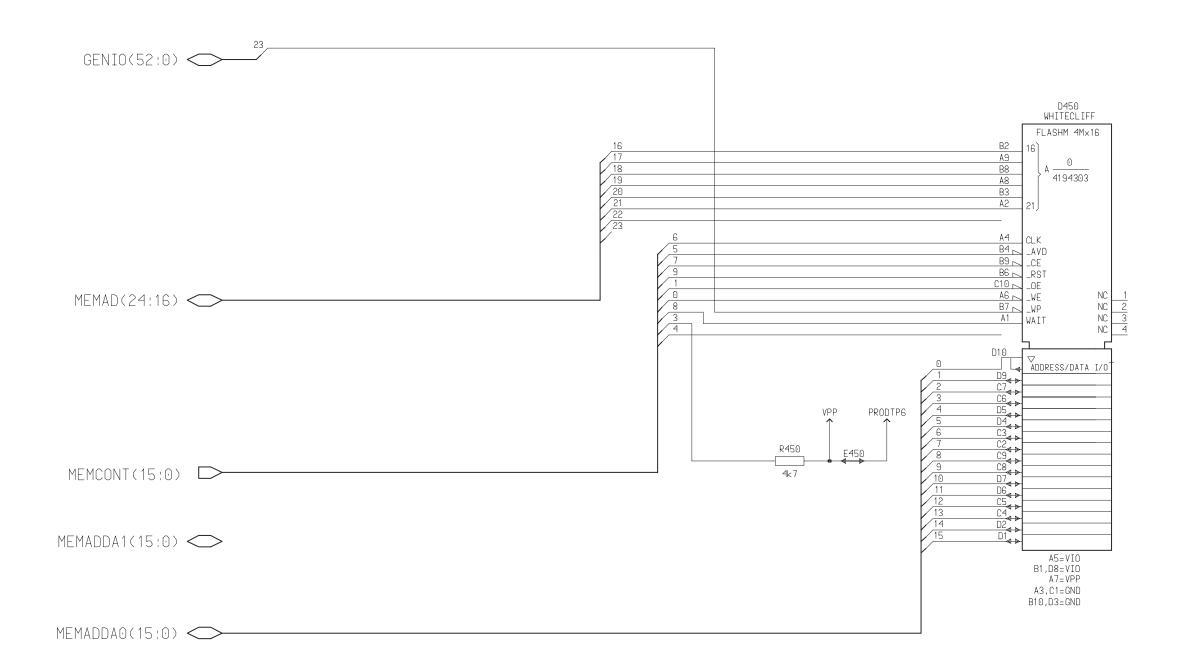


GSM RF - BB Interface (Version 1.3 Edit 35) for layout version 01



Flash Memory (Version 2.0 Edit 35) for layout version 01





Discrete capacitors for memory without VFlash1 (Version 1.3 Edit 10) for layout version 01

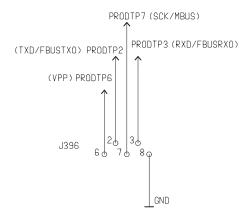


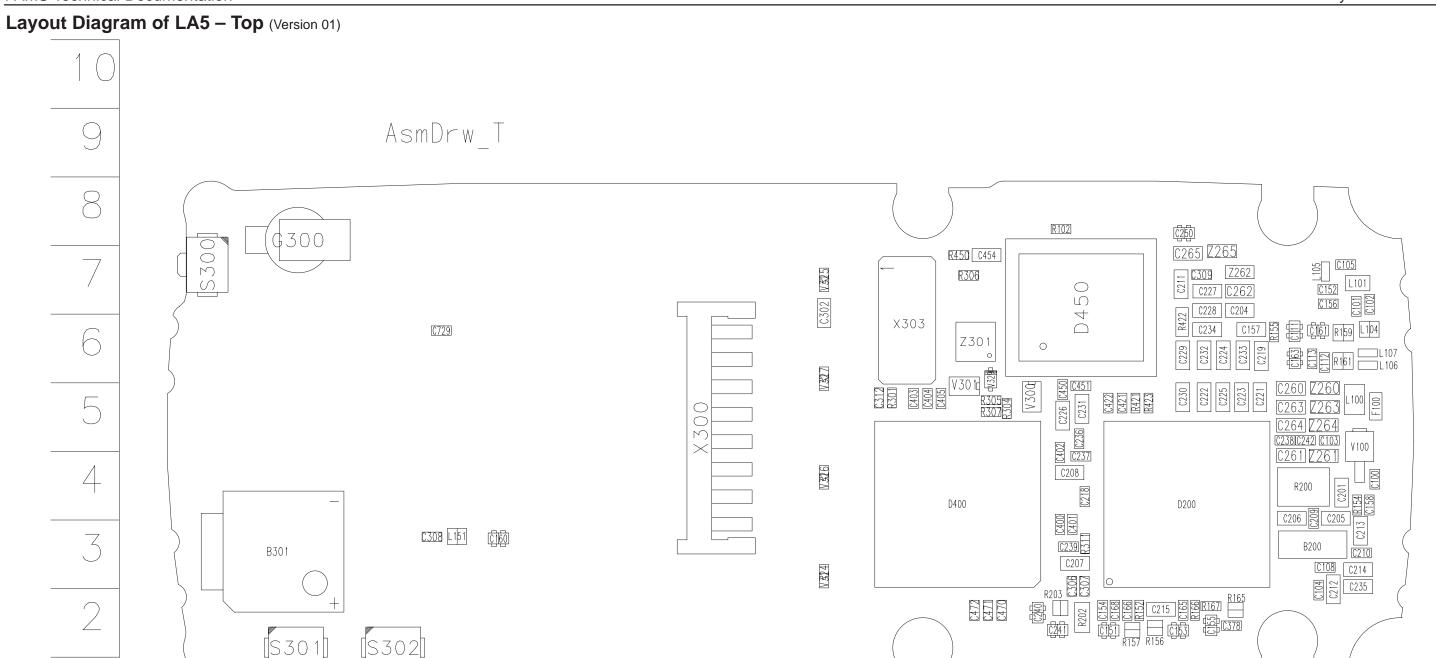
∨FLASH1



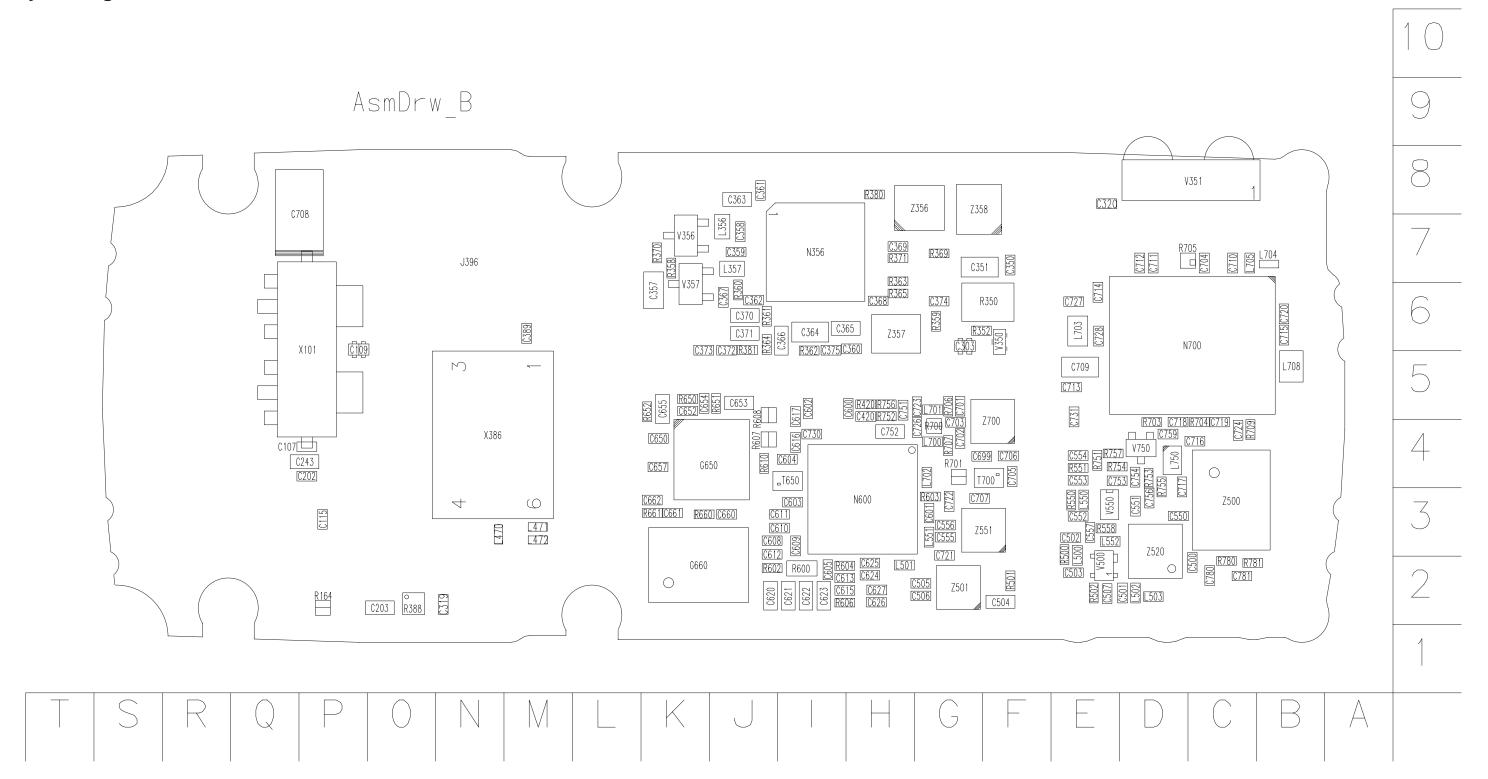
Test pattern – 5 pin (Version 2.0 Edit 12) for layout version 01

DUT



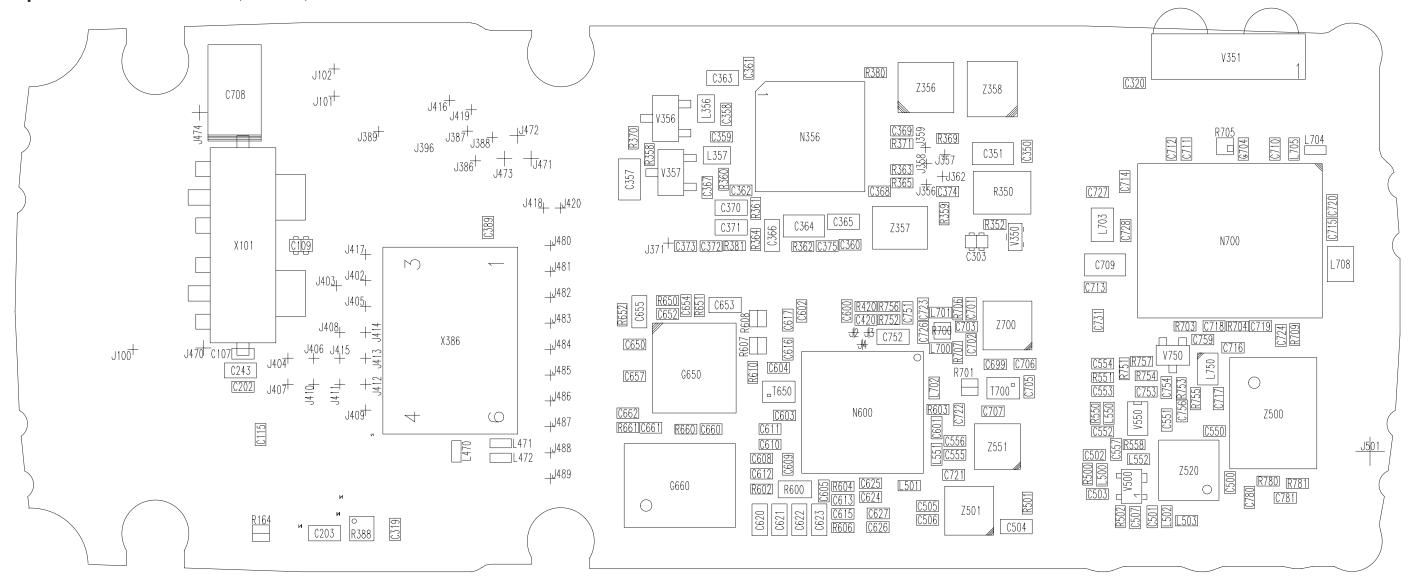


Layout Diagram of LA5 – Bottom (Version 01)

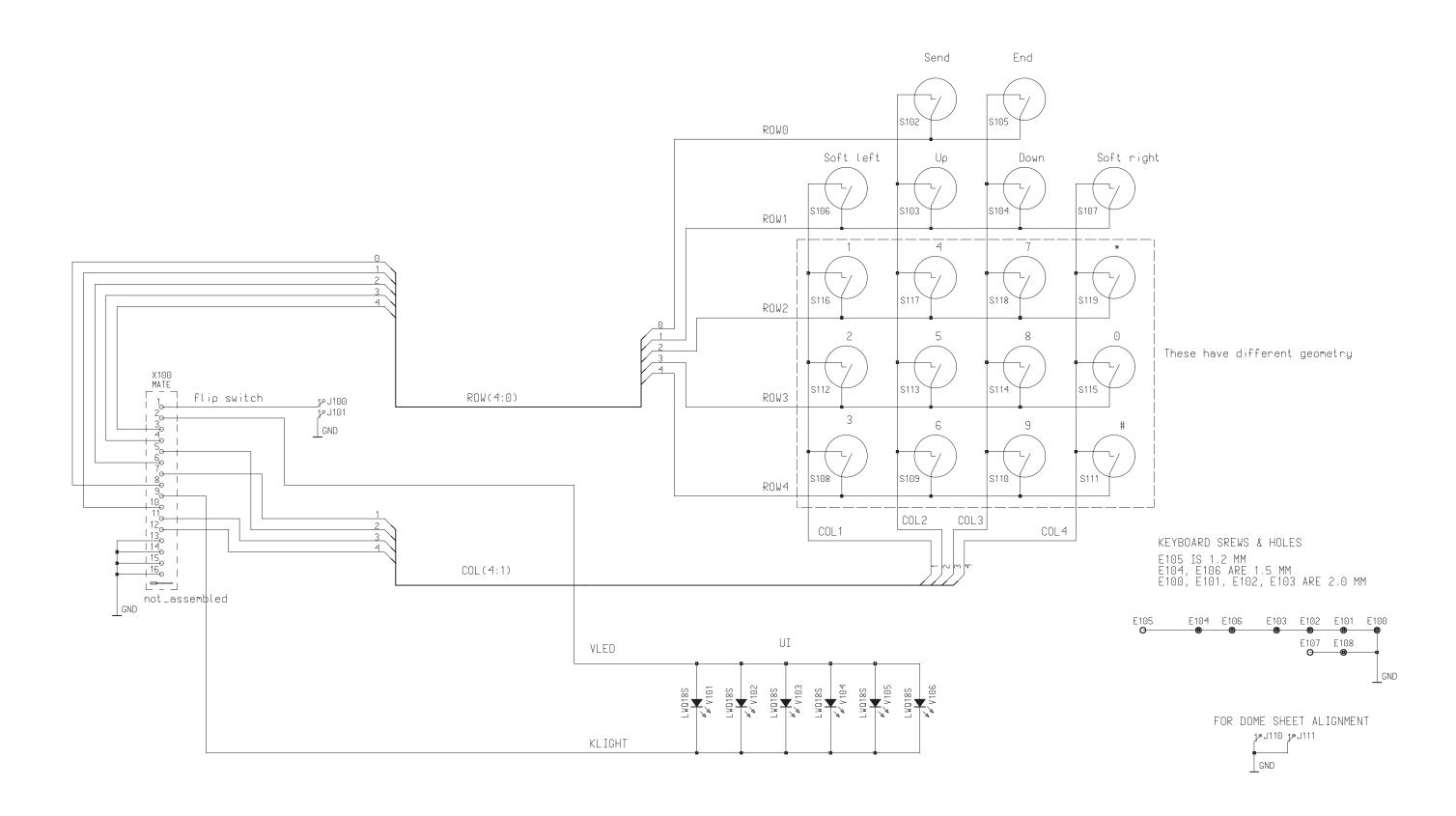


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Testpoints of LA5 – Bottom (Version 01)



UI Board - LU9 for version 11



Layout Diagram – LU9 for version 11

